ASIC DESIGN

Date:	**16/06/2016**
Technology:	**Design of Application Specific Integrated Circuits (ASICs) .**
TRL:	**The original application is not a space project. At LHC/CTA level probably TRL8-TRL9 equivalent**
Expert:	**D. Gascon**
Original Application:	**Astrophysics, High Energy Physics, Medical Imaging**
Projects:	**CTA, LHC, PET**
Key points:	**- Low power, high speed, low noise electronics design, both based on commercial component or ASICs. - Highly integrated and low power mixed-mode microelectronics design (ASIC).**
Potential Space Science Applications:	**- Development of general purpose highly integrated and low-power electronics is a key element for space mission and particularly for nanosats.**
Potential Applications:	**Any analog, digital or mixed-mode highly integrated electronics**